(A)OPEN CIRCUIT AND SHORT CIRCUIT TEST ON A 1- Ø TRANSFORMER

(B)LOAD TEST ON 1- Ø TRANSFORMER

Exp no: 5

Date

5(A) OPEN CIRCUIT AND SHORT CIRCUIT TEST ON A 1- Ø TRANSFORMER

<u>Aim</u>: (A) To determine the efficiency and regulation of a 1-Ø transformer by conducting open circuit test and short circuit test and to draw equivalent circuit. <u>Apparatus required</u>:

| S.no | Name of the Apparatus | Range | Туре | Quantity |
|------|-----------------------|--------------|-----------|----------|
| 1. | Ammeter | (0-5,10)A | MI | 1 |
| 2. | Volt meter | (0-150,300)V | MI | 1 |
| 3. | Wattmeter | 300V, 5A | EDM | 1 |
| | | 150 V,20A | EDM | 1 |
| 4. | Connecting | 2.5sq.mm | Copper | Few |
| | wires | | /Aluminum | |

Name plate details:

Precautions:

- 1. Auto Transformer should be in minimum voltage position at the time switching on the supply
- 2. Before switching off the supply the auto transformer should bring up to zero position.

Procedure:

OPEN CIRCUIT TEST:

- 1. Connections are made as per the circuit diagram.
- 2. After checking the minimum position of Autotransformer, DPST switch is closed.
- 3. Auto transformer variac is adjusted get the rated primary voltage.
- 4. Voltmeter, Ammeter and Wattmeter readings on primary side are noted.
- 5. Auto transformer is again brought to minimum position and DPST switch is opened.

SHORT CIRCUIT TEST:

- 1. Connections are made as per the circuit diagram.
- 2. After checking the minimum position of Autotransformer, DPST switch is closed.
- 3. Auto transformer variac is adjusted get the rated primary current.
- 4. Voltmeter, Ammeter and Wattmeter readings on primary side are noted.
- 5. Auto transformer is again brought to minimum position and DPST switch is opened.

Circuit diagram:

Open Circuit test:



Short circuit test:



Observation Tables:

Open Circuit test:

| Vo | Io | Wo |
|----|----|----|
| | | |
| | | |

Short circuit test:

| Vsc | Isc | Wsc | | |
|-----|-----|-----|--|--|
| | | | | |

Formulae used:

Core loss: $W_0 = V_0 I_0 \cos \phi_0$ $\begin{array}{ccc} W_{0} & & & \\ \cos \phi_{0} = & & \\ & & & \\$ $I_{\omega} = I_{o} \cos \phi_{o} (Amps) \qquad I_{\mu} = I_{o} \sin \phi_{o} (Amps)$ $R_{o2} = \frac{W_{sc}}{I_{sc}^{2}} \Omega \qquad Z_{o2} = \frac{V_{sc}}{I_{co}} \Omega \qquad X_{o2} = (Z_{02} - R_{o2}^{2})^{1/2}$ $\mathbf{R}_{o1} = \begin{array}{cc} \mathbf{R}_{02} & \mathbf{X}_{02} \\ -\mathbf{K}^2 & \mathbf{X}_{o1} = -\mathbf{K}^2 \\ \mathbf{K}^2 & \mathbf{V}_1 \end{array} \qquad \qquad \mathbf{K} = \begin{array}{c} \mathbf{V}_2 \\ -\mathbf{V}_1 \\ -\mathbf{V}_1 \end{array} = \text{Turns Ratio or Transformation ratio}$ Percentage efficiency (% η) = $\frac{xQ\cos\Phi}{xQ\cos\Phi+iron \ losses+copper \ losses} \times 100$ Percentage Regulation (%Reg) = $\frac{xIsc(R_{02}cos\phi \pm X_{02}sin\phi)}{V_2} \times 100$ or $\frac{xIsc(R_{01}cos\phi \pm X_{02}sin\phi)}{V_1} \times 100$ + For lagging power factor - For leading power factor

Where Q= KVA rating of the transformer

x is the load and it is 1 for full load,1/2 for half load,1/4 for quarter load etc.. And power factor is in unity,0.8 lag,0.8 lead etc...

Equivalent circuit diagram X₀₁ Isco R_{o1} MΛ ∞ R -Io L 0 $Z_L' = Z_L/K^2$ X ξ X_o R_o D Ν



Result:

5(B)Load test on 1-Ø Transformer

Aim: To determine the efficiency and voltage regulation of a single phase transformer by load test.

Apparatus Required:

| S.no | Name of the Apparatus | Range | Туре | Quantity |
|------|-----------------------------|----------------------|---------------------|----------|
| 1. | 1-phase Transformer | | | 1 |
| 2. | 1-phase Auto Transformer | (0-300V) Variable | | 1 |
| 3. | 1-Phase Resistive Load | 5KW,230V | Resistive | 1 |
| 4. | Ammeter | (0-5)A | MI | 1 |
| 5. | Volt meter | (0-300)V | MI | 1 |
| 6. | Wattmeter | 300 V, 10A | EDM Type | 1 |
| 7. | Connecting wires | 2.5sq.mm | Copper /Aluminum | Few |

Name plate details:

Procedure:

- 1. Connections are made as per the circuit diagram.
- 2. After checking the no load condition, minimum position of auto transformer and DPST switch is closed.
- 3. Ammeter, Voltmeter and Wattmeter readings on both primary side and secondary side are noted.
- 4. The load is increased and for each load, Voltmeter, Ammeter and Wattmeter readings on both primary and secondary sides are noted.
- 5. Again no load condition is obtained and DPST switch is open.

Circuit Diagram:



Observation Table:

| S.no | Load Curre nt | Primary | | | Secondary | | | %Efficiency (Output | % VR |
|------|---------------------|----------------------------|---------------------------|----------------------------|------------------------|-----------------------|------------------------|----------------------------|------|
| | | V ₁ (Vol ts) | I ₁ (Amp s) | V ₁ (Volt s) | V ₂ (Volts) | I ₁ (Amps) | W ₁ (Watts) | power/Input power)*100 | |
| | | | | | | | | | |
| | | | | | | | | | |
| | | | | | | | | | |
| | | | | | | | | | |
| | | | | | | | | | |

Formulae used:

Output Power = $W_2 x$ Multiplication factor

Input Power = W_1 x Multiplication factor

%VR= $\frac{Vnl - Vfl (Secondary)}{Vnl} x \ 100$

Model Graph:



Result: